

SIMATIC S7-1500F, CPU 1515F-2 PN, central processing unit with 1.5 MB work memory for program and 4.5 MB for data, 1st interface: PROFINET IRT with 2-port switch, 2nd interface: PROFINET RT, 6 ns bit performance, SIMATIC Memory Card required *** approvals and certificates according to entry 109817466 at to be considered! ***

General information	
Product type designation	CPU 1515F-2 PN
HW functional status	FS01
Firmware version	V3.0
<ul style="list-style-type: none"> FW update possible 	Yes
Product function	
<ul style="list-style-type: none"> I&M data 	Yes; I&M0 to I&M3
<ul style="list-style-type: none"> Isochronous mode 	Yes; Distributed and central; with minimum OB 6x cycle of 375 µs (distributed) and 1 ms (central)
Engineering with	
<ul style="list-style-type: none"> STEP 7 TIA Portal configurable/integrated from version 	V18 (FW V3.0); with older TIA Portal versions configurable as 6ES7515-2FM02-0AB0
Configuration control	
via dataset	Yes
Display	
Screen diagonal [cm]	6.1 cm
Control elements	
Number of keys	8
Mode buttons	2
Supply voltage	
Rated value (DC)	24 V
permissible range, lower limit (DC)	19.2 V
permissible range, upper limit (DC)	28.8 V
Reverse polarity protection	Yes
Mains buffering	
<ul style="list-style-type: none"> Mains/voltage failure stored energy time 	5 ms
<ul style="list-style-type: none"> Repeat rate, min. 	1/s
Input current	
Current consumption (rated value)	0.83 A
Current consumption, max.	1.03 A
Inrush current, max.	1.15 A; Rated value
I^2t	0.6 A ² s
Power	
Infeed power to the backplane bus	12 W
Power consumption from the backplane bus (balanced)	6.2 W
Power loss	
Power loss, typ.	7.9 W
Memory	
Number of slots for SIMATIC memory card	1
SIMATIC memory card required	Yes
Work memory	
<ul style="list-style-type: none"> integrated (for program) 	1.5 Mbyte
<ul style="list-style-type: none"> integrated (for data) 	4.5 Mbyte
Load memory	
<ul style="list-style-type: none"> Plug-in (SIMATIC Memory Card), max. 	32 Gbyte
Backup	
<ul style="list-style-type: none"> maintenance-free 	Yes
CPU processing times	
for bit operations, typ.	6 ns

for word operations, typ.	7 ns
for fixed point arithmetic, typ.	9 ns
for floating point arithmetic, typ.	37 ns
CPU-blocks	
Number of elements (total)	8 000; Blocks (OB, FB, FC, DB) and UDTs
DB	
• Number range	1 ... 60 999; subdivided into: number range that can be used by the user: 1 ... 59 999, and number range of DBs created via SFC 86: 60 000 ... 60 999
• Size, max.	4.5 Mbyte; For DBs with absolute addressing, the max. size is 64 KB
FB	
• Number range	0 ... 65 535
• Size, max.	1 Mbyte
FC	
• Number range	0 ... 65 535
• Size, max.	1 Mbyte
OB	
• Size, max.	1 Mbyte
• Number of free cycle OBs	100
• Number of time alarm OBs	20
• Number of delay alarm OBs	20
• Number of cyclic interrupt OBs	20; With minimum OB 3x cycle of 250 µs
• Number of process alarm OBs	50
• Number of DPV1 alarm OBs	3
• Number of isochronous mode OBs	2
• Number of technology synchronous alarm OBs	2
• Number of startup OBs	100
• Number of asynchronous error OBs	4
• Number of synchronous error OBs	2
• Number of diagnostic alarm OBs	1
Nesting depth	
• per priority class	24; Up to 8 possible for F-blocks
Counters, timers and their retentivity	
S7 counter	
• Number	2 048
Retentivity	
— adjustable	Yes
IEC counter	
• Number	Any (only limited by the main memory)
Retentivity	
— adjustable	Yes
S7 times	
• Number	2 048
Retentivity	
— adjustable	Yes
IEC timer	
• Number	Any (only limited by the main memory)
Retentivity	
— adjustable	Yes
Data areas and their retentivity	
Retentive data area (incl. timers, counters, flags), max.	512 kbyte; In total; available retentive memory for bit memories, timers, counters, DBs, and technology data (axes): 472 KB
Extended retentive data area (incl. timers, counters, flags), max.	4.5 Mbyte; When using PS 6 0W 24/48/60 V DC HF
Flag	
• Size, max.	16 kbyte
• Number of clock memories	8; 8 clock memory bit, grouped into one clock memory byte
Data blocks	
• Retentivity adjustable	Yes
• Retentivity preset	No
Local data	
• per priority class, max.	64 kbyte; max. 16 KB per block
Address area	

Number of IO modules	8 192; max. number of modules / submodules
I/O address area	
• Inputs	32 kbyte; All inputs are in the process image
• Outputs	32 kbyte; All outputs are in the process image
per integrated IO subsystem	
— Inputs (volume)	8 kbyte
— Outputs (volume)	8 kbyte
per CM/CP	
— Inputs (volume)	8 kbyte
— Outputs (volume)	8 kbyte
Subprocess images	
• Number of subprocess images, max.	32
Hardware configuration	
Number of distributed IO systems	64; A distributed I/O system is characterized not only by the integration of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link)
Number of DP masters	
• Via CM	8; A maximum of 8 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total
Number of IO Controllers	
• integrated	2
• Via CM	8; A maximum of 8 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total
Rack	
• Modules per rack, max.	32; CPU + 31 modules
• Number of lines, max.	1
PtP CM	
• Number of PtP CMs	the number of connectable PtP CMs is only limited by the number of available slots
Time of day	
Clock	
• Type	Hardware clock
• Backup time	6 wk; At 40 °C ambient temperature, typically
• Deviation per day, max.	10 s; Typ.: 2 s
Operating hours counter	
• Number	16
Clock synchronization	
• supported	Yes
• in AS, master	Yes
• in AS, slave	Yes
• on Ethernet via NTP	Yes
Interfaces	
Number of PROFINET interfaces	2
1. Interface	
Interface types	
• RJ 45 (Ethernet)	Yes; X1
• Number of ports	2
• integrated switch	Yes
Protocols	
• IP protocol	Yes; IPv4
• PROFINET IO Controller	Yes
• PROFINET IO Device	Yes
• SIMATIC communication	Yes
• Open IE communication	Yes; Optionally also encrypted
• Web server	Yes
• Media redundancy	Yes
PROFINET IO Controller	
Services	
— PG/OP communication	Yes
— Isochronous mode	Yes
— Direct data exchange	Yes; Requirement: IRT and isochronous mode (MRPD optional)
— IRT	Yes