SIEMENS

Data sheet for Terminal Board

Article No. : 6SL3055-0AA00-2TA0

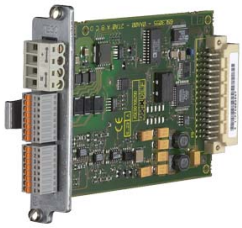

Figure similar

Client order no. :
Order no. :
Offer no. :
Remarks :

Item no. :
Consignment no. :
Project :

Inputs / outputs	
Digital inputs [1]	
Number	4
Voltage	-3 ... 30 V
Low level	-3 ... 5 V
High level	15 ... 30 V
Current consumption at 24 V DC	6 mA
Delay time L→H, typ. [2]	50 µs
Delay time H→L, typ. [2]	100 µs
Conductor cross-section, max.	0.5 mm² (AWG 21)
Digital outputs	
Number	4
Voltage	24 V DC
Load current, max.	500 mA
Delay time, approx. [3]	150 µs
Conductor cross-section, max.	0.5 mm² (AWG 21)
Analog inputs	
Number	2
Voltage	-10 ... 10 V
Internal resistor	65 kOhm
Resolution [4]	13 bit + sign
Conductor cross-section, max.	0.5 mm² (AWG 21)
Analog outputs	
Number	2
Voltage	-10 ... 10 V
Load current, max.	-3 ... 3 mA
Resolution	11 bit + sign
Delay time, approx.	200 µs
Conductor cross-section, max.	0.5 mm² (AWG 21)

Electrical data	
Consumed current at 24 V DC, max.	0.05 A
Conductor cross-section, max.	2.5 mm² (AWG 14)
Protection, max.	20 A
Power loss, max.	3 W

Mechanical data	
Net weight	0.1 kg (0.22 lb)

Standards	
Certificate of suitability	cULus

[1] In accordance with IEC 61131-2 Type 1

[2] The specified delay times refer to the hardware. The actual reaction time depends on the time slot in which the digital input or output is processed.

[3] The specified delay times refer to the hardware. The actual reaction time depends on the time slot in which the digital input or output is processed.

[4] If the analog input is to be operated in the signal processing sense with continuously variable input voltage, the sampling frequency fa = 1/t time slice must be at least twice the value of the highest signal frequency fmax.