



SIMATIC S7-1500F, CPU 1515F-2 PN, central processing unit with work memory 750 KB for program and 3 MB for data, 1st interface: PROFINET IRT with 2-port switch, 2nd interface: PROFINET RT, 30 ns bit performance, SIMATIC Memory Card required

General information	
Product type designation	CPU 1515F-2 PN
HW functional status	FS01
Firmware version	V2.9
Product function	
<ul style="list-style-type: none"> <li>I&amp;M data</li> </ul>	Yes; I&M0 to I&M3
<ul style="list-style-type: none"> <li>Isochronous mode</li> </ul>	Yes; Distributed and central; with minimum OB 6x cycle of 500 $\mu$ s (distributed) and 1 ms (central)
Engineering with	
<ul style="list-style-type: none"> <li>STEP 7 TIA Portal configurable/integrated from version</li> </ul>	V17 (FW V2.9) / V16 (FW V2.8) or higher; with older TIA Portal versions configurable as 6ES7515-2FM01-0AB0
Configuration control	
via dataset	Yes
Display	
Screen diagonal [cm]	6.1 cm
Control elements	
Number of keys	8
Mode buttons	2
Supply voltage	
Rated value (DC)	24 V
permissible range, lower limit (DC)	19.2 V
permissible range, upper limit (DC)	28.8 V
Reverse polarity protection	Yes
Mains buffering	
<ul style="list-style-type: none"> <li>Mains/voltage failure stored energy time</li> </ul>	5 ms
<ul style="list-style-type: none"> <li>Repeat rate, min.</li> </ul>	1/s
Input current	
Current consumption (rated value)	0.8 A
Current consumption, max.	1.1 A
Inrush current, max.	2.4 A; Rated value
$I^2t$	0.02 A <sup>2</sup> ·s
Power	
Infeed power to the backplane bus	12 W
Power consumption from the backplane bus (balanced)	6.2 W
Power loss	
Power loss, typ.	6.3 W
Memory	
Number of slots for SIMATIC memory card	1
SIMATIC memory card required	Yes
Work memory	

<ul style="list-style-type: none"> <li>integrated (for program)</li> </ul>	750 kbyte
<ul style="list-style-type: none"> <li>integrated (for data)</li> </ul>	3 Mbyte
<b>Load memory</b>	
<ul style="list-style-type: none"> <li>Plug-in (SIMATIC Memory Card), max.</li> </ul>	32 Gbyte
<b>Backup</b>	
<ul style="list-style-type: none"> <li>maintenance-free</li> </ul>	Yes
<b>CPU processing times</b>	
for bit operations, typ.	30 ns
for word operations, typ.	36 ns
for fixed point arithmetic, typ.	48 ns
for floating point arithmetic, typ.	192 ns
<b>CPU-blocks</b>	
Number of elements (total)	8 000; Blocks (OB, FB, FC, DB) and UDTs
<b>DB</b>	
<ul style="list-style-type: none"> <li>Number range</li> </ul>	1 ... 60 999; subdivided into: number range that can be used by the user: 1 ... 59 999, and number range of DBs created via SFC 86: 60 000 ... 60 999
<ul style="list-style-type: none"> <li>Size, max.</li> </ul>	3 Mbyte; For DBs with absolute addressing, the max. size is 64 KB
<b>FB</b>	
<ul style="list-style-type: none"> <li>Number range</li> </ul>	0 ... 65 535
<ul style="list-style-type: none"> <li>Size, max.</li> </ul>	500 kbyte
<b>FC</b>	
<ul style="list-style-type: none"> <li>Number range</li> </ul>	0 ... 65 535
<ul style="list-style-type: none"> <li>Size, max.</li> </ul>	500 kbyte
<b>OB</b>	
<ul style="list-style-type: none"> <li>Size, max.</li> </ul>	500 kbyte
<ul style="list-style-type: none"> <li>Number of free cycle OBs</li> </ul>	100
<ul style="list-style-type: none"> <li>Number of time alarm OBs</li> </ul>	20
<ul style="list-style-type: none"> <li>Number of delay alarm OBs</li> </ul>	20
<ul style="list-style-type: none"> <li>Number of cyclic interrupt OBs</li> </ul>	20; With minimum OB 3x cycle of 500 µs
<ul style="list-style-type: none"> <li>Number of process alarm OBs</li> </ul>	50
<ul style="list-style-type: none"> <li>Number of DPV1 alarm OBs</li> </ul>	3
<ul style="list-style-type: none"> <li>Number of isochronous mode OBs</li> </ul>	2
<ul style="list-style-type: none"> <li>Number of technology synchronous alarm OBs</li> </ul>	2
<ul style="list-style-type: none"> <li>Number of startup OBs</li> </ul>	100
<ul style="list-style-type: none"> <li>Number of asynchronous error OBs</li> </ul>	4
<ul style="list-style-type: none"> <li>Number of synchronous error OBs</li> </ul>	2
<ul style="list-style-type: none"> <li>Number of diagnostic alarm OBs</li> </ul>	1
<b>Nesting depth</b>	
<ul style="list-style-type: none"> <li>per priority class</li> </ul>	24; Up to 8 possible for F-blocks
<b>Counters, timers and their retentivity</b>	
<b>S7 counter</b>	
<ul style="list-style-type: none"> <li>Number</li> </ul>	2 048
Retentivity	
— adjustable	Yes
<b>IEC counter</b>	
<ul style="list-style-type: none"> <li>Number</li> </ul>	Any (only limited by the main memory)
Retentivity	
— adjustable	Yes
<b>S7 times</b>	
<ul style="list-style-type: none"> <li>Number</li> </ul>	2 048
Retentivity	
— adjustable	Yes
<b>IEC timer</b>	
<ul style="list-style-type: none"> <li>Number</li> </ul>	Any (only limited by the main memory)
Retentivity	
— adjustable	Yes
<b>Data areas and their retentivity</b>	
Retentive data area (incl. timers, counters, flags), max.	512 kbyte; In total; available retentive memory for bit memories, timers, counters, DBs, and technology data (axes): 472 KB
Extended retentive data area (incl. timers, counters, flags), max.	3 Mbyte; When using PS 6 0W 24/48/60 V DC HF
<b>Flag</b>	
<ul style="list-style-type: none"> <li>Size, max.</li> </ul>	16 kbyte

<ul style="list-style-type: none"> <li>Number of clock memories</li> </ul>	8; 8 clock memory bit, grouped into one clock memory byte
<b>Data blocks</b>	
<ul style="list-style-type: none"> <li>Retentivity adjustable</li> <li>Retentivity preset</li> </ul>	Yes No
<b>Local data</b>	
<ul style="list-style-type: none"> <li>per priority class, max.</li> </ul>	64 kbyte; max. 16 KB per block
<b>Address area</b>	
Number of IO modules	8 192; max. number of modules / submodules
<b>I/O address area</b>	
<ul style="list-style-type: none"> <li>Inputs</li> <li>Outputs</li> </ul>	32 kbyte; All inputs are in the process image 32 kbyte; All outputs are in the process image
<b>per integrated IO subsystem</b>	
<ul style="list-style-type: none"> <li>Inputs (volume)</li> <li>Outputs (volume)</li> </ul>	8 kbyte 8 kbyte
<b>per CM/CP</b>	
<ul style="list-style-type: none"> <li>Inputs (volume)</li> <li>Outputs (volume)</li> </ul>	8 kbyte 8 kbyte
<b>Subprocess images</b>	
<ul style="list-style-type: none"> <li>Number of subprocess images, max.</li> </ul>	32
<b>Hardware configuration</b>	
Number of distributed IO systems	64; A distributed I/O system is characterized not only by the integration of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link)
<b>Number of DP masters</b>	
<ul style="list-style-type: none"> <li>Via CM</li> </ul>	8; A maximum of 8 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total
<b>Number of IO Controllers</b>	
<ul style="list-style-type: none"> <li>integrated</li> <li>Via CM</li> </ul>	2 8; A maximum of 8 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total
<b>Rack</b>	
<ul style="list-style-type: none"> <li>Modules per rack, max.</li> <li>Number of lines, max.</li> </ul>	32; CPU + 31 modules 1
<b>PtP CM</b>	
<ul style="list-style-type: none"> <li>Number of PtP CMs</li> </ul>	the number of connectable PtP CMs is only limited by the number of available slots
<b>Time of day</b>	
<b>Clock</b>	
<ul style="list-style-type: none"> <li>Type</li> <li>Backup time</li> <li>Deviation per day, max.</li> </ul>	Hardware clock 6 wk; At 40 °C ambient temperature, typically 10 s; Typ.: 2 s
<b>Operating hours counter</b>	
<ul style="list-style-type: none"> <li>Number</li> </ul>	16
<b>Clock synchronization</b>	
<ul style="list-style-type: none"> <li>supported</li> <li>in AS, master</li> <li>in AS, slave</li> <li>on Ethernet via NTP</li> </ul>	Yes Yes Yes Yes
<b>Interfaces</b>	
Number of PROFINET interfaces	2
<b>1. Interface</b>	
<b>Interface types</b>	
<ul style="list-style-type: none"> <li>RJ 45 (Ethernet)</li> <li>Number of ports</li> <li>integrated switch</li> </ul>	Yes; X1 2 Yes
<b>Protocols</b>	
<ul style="list-style-type: none"> <li>IP protocol</li> <li>PROFINET IO Controller</li> <li>PROFINET IO Device</li> <li>SIMATIC communication</li> <li>Open IE communication</li> <li>Web server</li> <li>Media redundancy</li> </ul>	Yes; IPv4 Yes Yes Yes Yes; Optionally also encrypted Yes Yes; MRP Automanager according to IEC 62439-2 Edition 2.0