



SIMATIC DP, CPU 1512SP F-1 PN for ET 200SP, Central processing unit with Work memory 300 KB for program and 1 MB for data, 1st interface: PROFINET IRT with 3-port switch, 48 ns bit performance, SIMATIC Memory Card required, BusAdapter required for Port 1 and 2

General information	
Product type designation	CPU 1512SP F-1 PN
HW functional status	FS05
Firmware version	V2.9
Product function	
<ul style="list-style-type: none"> I&M data 	Yes; I&M0 to I&M3
<ul style="list-style-type: none"> Module swapping during operation (hot swapping) 	Yes; Multi-hot swapping
<ul style="list-style-type: none"> Isochronous mode 	Yes; Only with PROFINET; with minimum OB 6x cycle of 625 µs
Engineering with	
<ul style="list-style-type: none"> STEP 7 TIA Portal configurable/integrated from version 	V17 (FW V2.9) / V13 SP1 Update 4 (FW V1.8) or higher
Configuration control	
via dataset	Yes
Control elements	
Mode selector switch	1
Supply voltage	
Type of supply voltage	24 V DC
permissible range, lower limit (DC)	19.2 V
permissible range, upper limit (DC)	28.8 V
Reverse polarity protection	Yes
Mains buffering	
<ul style="list-style-type: none"> Mains/voltage failure stored energy time 	5 ms
Input current	
Current consumption (rated value)	0.6 A
Current consumption, max.	0.9 A
Inrush current, max.	4.7 A; Rated value
I^2t	0.14 A ² ·s
Power	
Infeed power to the backplane bus	8.75 W
Power loss	
Power loss, typ.	5.6 W
Memory	
Number of slots for SIMATIC memory card	1
SIMATIC memory card required	Yes
Work memory	
<ul style="list-style-type: none"> integrated (for program) 	300 kbyte
<ul style="list-style-type: none"> integrated (for data) 	1 Mbyte

Load memory	
• Plug-in (SIMATIC Memory Card), max.	32 Gbyte
Backup	
• maintenance-free	Yes
CPU processing times	
for bit operations, typ.	48 ns
for word operations, typ.	58 ns
for fixed point arithmetic, typ.	77 ns
for floating point arithmetic, typ.	307 ns
CPU-blocks	
Number of elements (total)	4 000; Blocks (OB, FB, FC, DB) and UDTs
DB	
• Number range	1 ... 60 999; subdivided into: number range that can be used by the user: 1 ... 59 999, and number range of DBs created via SFC 86: 60 000 ... 60 999
• Size, max.	1 Mbyte; For DBs with absolute addressing, the max. size is 64 KB
FB	
• Number range	0 ... 65 535
• Size, max.	200 kbyte
FC	
• Number range	0 ... 65 535
• Size, max.	200 kbyte
OB	
• Size, max.	200 kbyte
• Number of free cycle OBs	100
• Number of time alarm OBs	20
• Number of delay alarm OBs	20
• Number of cyclic interrupt OBs	20; With minimum OB 3x cycle of 500 µs
• Number of process alarm OBs	50
• Number of DPV1 alarm OBs	3
• Number of isochronous mode OBs	1
• Number of technology synchronous alarm OBs	2
• Number of startup OBs	100
• Number of asynchronous error OBs	4
• Number of synchronous error OBs	2
• Number of diagnostic alarm OBs	1
Nesting depth	
• per priority class	24; Up to 8 possible for F-blocks
Counters, timers and their retentivity	
S7 counter	
• Number	2 048
Retentivity	
— adjustable	Yes
IEC counter	
• Number	Any (only limited by the main memory)
Retentivity	
— adjustable	Yes
S7 times	
• Number	2 048
Retentivity	
— adjustable	Yes
IEC timer	
• Number	Any (only limited by the main memory)
Retentivity	
— adjustable	Yes
Data areas and their retentivity	
Retentive data area (incl. timers, counters, flags), max.	128 kbyte; Available retentive memory for bit memories, timers, counters, DBs, and technology data (axes): 88 KB

Flag	
<ul style="list-style-type: none"> • Size, max. • Number of clock memories 	<p>16 kbyte</p> <p>8; 8 clock memory bit, grouped into one clock memory byte</p>
Data blocks	
<ul style="list-style-type: none"> • Retentivity adjustable • Retentivity preset 	<p>Yes</p> <p>No</p>
Local data	
<ul style="list-style-type: none"> • per priority class, max. 	64 kbyte; max. 16 KB per block
Address area	
Number of IO modules	2 048; max. number of modules / submodules
I/O address area	
<ul style="list-style-type: none"> • Inputs • Outputs 	<p>32 kbyte; All inputs are in the process image</p> <p>32 kbyte; All outputs are in the process image</p>
per integrated IO subsystem	
— Inputs (volume)	8 kbyte
— Outputs (volume)	8 kbyte
per CM/CP	
— Inputs (volume)	8 kbyte
— Outputs (volume)	8 kbyte
Subprocess images	
<ul style="list-style-type: none"> • Number of subprocess images, max. 	32
Address space per module	
<ul style="list-style-type: none"> • Address space per module, max. 	288 byte; For input and output data respectively
Address space per station	
<ul style="list-style-type: none"> • Address space per station, max. 	2 560 byte; for central inputs and outputs; depending on configuration; 2 048 bytes for ET 200SP modules + 512 bytes for ET 200AL modules
Hardware configuration	
Number of distributed IO systems	32; A distributed I/O system is characterized not only by the integration of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link)
Number of DP masters	
<ul style="list-style-type: none"> • Via CM 	1
Number of IO Controllers	
<ul style="list-style-type: none"> • integrated • Via CM 	<p>1</p> <p>0</p>
Rack	
<ul style="list-style-type: none"> • Modules per rack, max. • Quantity of operable ET 200SP modules, max. • Quantity of operable ET 200AL modules, max. • Number of lines, max. 	<p>80; CPU + 64 modules + server module (mounting width max. 1 m) + 16 ET 200AL modules</p> <p>64</p> <p>16</p> <p>1</p>
PtP CM	
<ul style="list-style-type: none"> • Number of PtP CMs 	the number of connectable PtP CMs is only limited by the number of available slots
Time of day	
Clock	
<ul style="list-style-type: none"> • Type • Backup time • Deviation per day, max. 	<p>Hardware clock</p> <p>6 wk; At 40 °C ambient temperature, typically</p> <p>10 s; Typ.: 2 s</p>
Operating hours counter	
<ul style="list-style-type: none"> • Number 	16
Clock synchronization	
<ul style="list-style-type: none"> • supported • to DP, master • to DP, slave • in AS, master • in AS, slave • on Ethernet via NTP 	<p>Yes</p> <p>Yes; Via CM DP module</p> <p>Yes; Via CM DP module</p> <p>Yes</p> <p>Yes</p> <p>Yes</p>