

SIMATIC S7-1200, CPU 1215C, compact CPU, DC/DC/DC, 2 PROFINET ports, onboard I/O: 14 DI 24 V DC; 10 DO 24 V DC; 0.5A; 2 AI 0-10 V DC, 2 AO 0-20 mA DC, Power supply: DC 20.4-28.8V DC, Program/data memory 125 KB



General information	
Product type designation	CPU 1215C DC/DC/DC
Firmware version	V4.2
Engineering with	
<ul style="list-style-type: none"> Programming package 	STEP 7 V14 or higher
Supply voltage	
Rated value (DC)	
<ul style="list-style-type: none"> 24 V DC 	Yes
permissible range, lower limit (DC)	20.4 V
permissible range, upper limit (DC)	28.8 V
Reverse polarity protection	Yes
Load voltage L+	
<ul style="list-style-type: none"> Rated value (DC) 	24 V
<ul style="list-style-type: none"> permissible range, lower limit (DC) 	20.4 V
<ul style="list-style-type: none"> permissible range, upper limit (DC) 	28.8 V
Input current	
Current consumption (rated value)	500 mA; CPU only
Current consumption, max.	1 500 mA; CPU with all expansion modules

Inrush current, max.	12 A; at 28.8 V DC
I^2t	0.5 A ² ·s
Output current	
for backplane bus (5 V DC), max.	1 600 mA; Max. 5 V DC for SM and CM
Encoder supply	
24 V encoder supply	
• 24 V	L+ minus 4 V DC min.
Power loss	
Power loss, typ.	12 W
Memory	
Work memory	
• integrated	125 kbyte
• expandable	No
Load memory	
• integrated	4 Mbyte
• Plug-in (SIMATIC Memory Card), max.	with SIMATIC memory card
Backup	
• present	Yes
• maintenance-free	Yes
• without battery	Yes
CPU processing times	
for bit operations, typ.	0.08 µs; / instruction
for word operations, typ.	1.7 µs; / instruction
for floating point arithmetic, typ.	2.3 µs; / instruction
CPU-blocks	
Number of blocks (total)	DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used
OB	
• Number, max.	Limited only by RAM for code
Data areas and their retentivity	
Retentive data area (incl. timers, counters, flags), max.	10 kbyte
Flag	
• Number, max.	8 kbyte; Size of bit memory address area
Local data	
• per priority class, max.	16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB
Address area	
Process image	

- Inputs, adjustable
- Outputs, adjustable

1 kbyte

1 kbyte

Hardware configuration

Number of modules per system, max.

3 comm. modules, 1 signal board, 8 signal modules

Time of day

Clock

- Hardware clock (real-time)
- Backup time
- Deviation per day, max.

Yes

480 h; Typical

±60 s/month at 25 °C

Digital inputs

Number of digital inputs

14; Integrated

- of which inputs usable for technological functions

6; HSC (High Speed Counting)

Source/sink input

Yes

Number of simultaneously controllable inputs

all mounting positions

— up to 40 °C, max.

14

Input voltage

- Rated value (DC)
- for signal "0"
- for signal "1"

24 V

5 V DC at 1 mA

15 V DC at 2.5 mA

Input delay (for rated value of input voltage)

for standard inputs

— parameterizable

0.2 ms, 0.4 ms, 0.8 ms, 1.6 ms, 3.2 ms, 6.4 ms and 12.8 ms, selectable in groups of four

— at "0" to "1", min.

0.2 ms

— at "0" to "1", max.

12.8 ms

for interrupt inputs

— parameterizable

Yes

for technological functions

— parameterizable

Single phase: 3 @ 100 kHz & 3 @ 30 kHz, differential: 3 @ 80 kHz & 3 @ 30 kHz

Cable length

- shielded, max.
- unshielded, max.

500 m; 50 m for technological functions

300 m; For technological functions: No

Digital outputs

Number of digital outputs

10

- of which high-speed outputs

4; 100 kHz Pulse Train Output

Limitation of inductive shutdown voltage to

L+ (-48 V)

Switching capacity of the outputs

- with resistive load, max.

0.5 A